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      62:SPIN(R) 1975-2005/Mar W3
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Set
        Items
                Description
                SERIAL (1W) PARALLEL
S1
         5635
S2
                SP OR S()P
       381303
                (SHIFT OR SCAN) (1W) REGISTER? ? OR SCAN(1W) (INPUT? ? OR PIN?
S3
        18138
              ? OR CIRCUIT? ? OR PATH? ? OR PATHWAY? ?)
S4
                MULTIPLEXOR? ? OR MULTIPLEXER? ? OR MUX OR MUXS
                 (2ND OR SECOND? OR TWO OR DUAL OR OTHER OR ANOTHER OR DIFF-
S5
             ERENT OR SEPARATE) (2W) S4
S6
          544
                S4(2W)(TWO OR 2 OR B)
                 (TEST??? OR SCAN????) (5N) (CIRCUIT? ? OR CHIP? ? OR IC OR I-
S7
       150114
             CS OR MICROCHIP? ? OR SEMICONDUCTOR? ?)
                S1:S2 AND S3 AND S4 AND S7
S8
S9
                S1:S2 AND S3 AND S4
S10
                RD (unique items)
```

S10 NOT PY=2002:2005

S11

File 347:JAPIO Nov 1976-2005/Jan(Updated 050506) (c) 2005 JPO & JAPIO File 350:Derwent WPIX 1963-2005/UD,UM &UP=200535 (c) 2005 Thomson Derwent

Set	Items	Description
S1	7212	SERIAL (1W) PARALLEL
S2	33524	SP OR S()P
S3	38931	(SHIFT OR SCAN) () REGISTER? ?
S4	46388	MULTIPLEXOR? ? OR MULTIPLEXER? ? OR MUX OR MUXS
S5	2945	(2ND OR SECOND? OR TWO OR DUAL OR OTHER OR ANOTHER) (3W) S4
S6	4	S1:S2 AND S3 AND S5
S7	101	(DIFFERENT OR SEPARATE) (2W)S4
S8	0	S1:S2 AND S3 AND S7

File 347:JAPIO Nov 1976-2005/Jan(Updated 050506) (c) 2005 JPO & JAPIO File 350:Derwent WPIX 1963-2005/UD,UM &UP=200535 (c) 2005 Thomson Derwent

Set S1	Items 7212	Description SERIAL(1W)PARALLEL
S2	33524	SP OR S()P
S3	41815	(SHIFT OR SCAN) (1W) REGISTER? ? OR SCAN(1W) (INPUT? ? OR PIN?
	?	OR CIRCUIT? ? OR PATH? ? OR PATHWAY? ?)
S4	46388	MULTIPLEXOR? ? OR MULTIPLEXER? ? OR MUX OR MUXS
S5	85965	(TEST??? OR SCAN????) (5N) (CIRCUIT? ? OR CHIP? ? OR IC OR I-
	CS	OR MICROCHIP? ? OR SEMICONDUCTOR? ?)
S6	3	S1:S2 AND S3 AND S4 AND S5
S7	57 -	S1:S2 AND S3 AND S5
S8	246	S3 AND S4 AND S5
S9	3	S6

File 348: EUROPEAN PATENTS 1978-2005/May W04 (c) 2005 European Patent Office File 349:PCT FULLTEXT 1979-2005/UB=20050602,UT=20050526 (c) 2005 WIPO/Univentio Set Items Description S1 9692 SERIAL (1W) PARALLEL SP OR S()P S2 129795 (SHIFT OR SCAN) (1W) REGISTER? ? OR SCAN(1W) (INPUT? ? OR PIN? S3 20289 ? OR CIRCUIT? ? OR PATH? ? OR PATHWAY? ?) MULTIPLEXOR? ? OR MULTIPLEXER? ? OR MUX OR MUXS S4 46468 (2ND OR SECOND? OR TWO OR DUAL OR OTHER OR ANOTHER OR DIFF-S5 ERENT OR SEPARATE) (2W) S4 S6 S4(2W)(TWO OR 2 OR B) 3582 S7 (TEST??? OR SCAN????) (5N) (CIRCUIT? ? OR CHIP? ? OR IC OR I-31580 CS OR MICROCHIP? ? OR SEMICONDUCTOR? ?)

S1:S2(50N)S3(50N)S4(50N)S7

S9 AND AY=(1976:2001)/PR

S1:S2(100N)S3(100N)S4(100N)S7

8

20

19

S8

S9

S10